

APM32F411

Errata Sheet

Version: V1.1

Contents

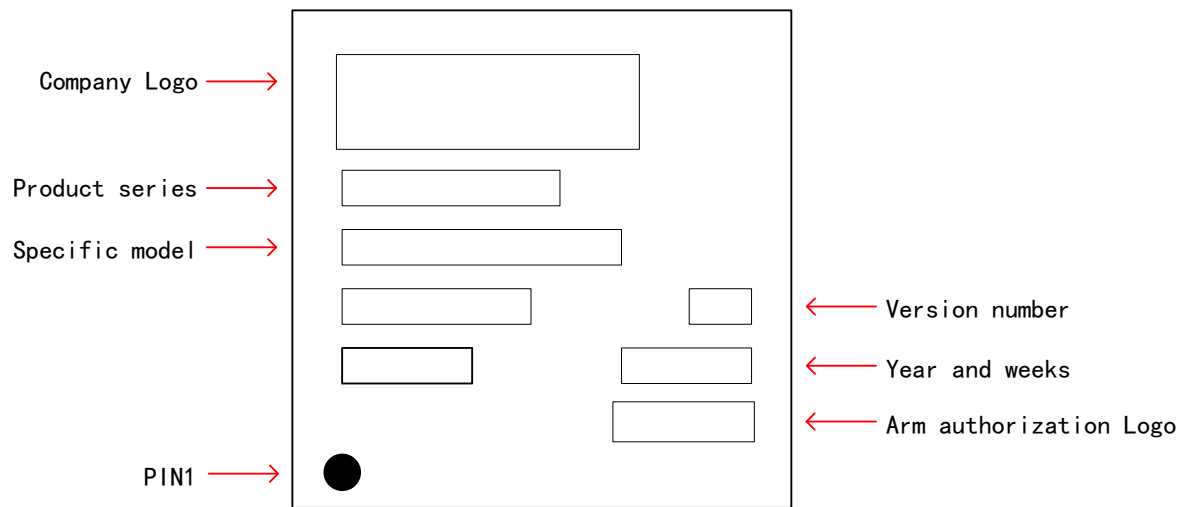
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1 Introduction

This Manual mainly introduces the limitations of the APM32F411 series products during use. If you encounter the application scenarios described in the manual during the use of the product, please use the product according to the solutions provided in the manual; if no solution is provided, please avoid this application scenario.

2 Product Version and Silk Screen Printing Instructions

Figure 1 Product Version and Silk Screen Printing Instructions



3 Errata List

Table 1 Errata List

Category	Introduction	Product version
		A1
Flash	Read protection abnormality	√
ADC	ADC2 cannot output normally	√

Note: √ indicates present, × indicates not present.

4 Flash

4.1 Read protection abnormal

Description

When using ISP upgrade, part of the RAM will be occupied, leading to read protection abnormality.

Solutions

1. It is not recommended to use ISP upgrade.
2. If ISP upgrade is used, it is recommended to run code on FLASH.

5 ADC

5.1 ADC2 cannot output normally

Description

ADC1/2 use separate clocks ADC1_CLK and ADC2_CLK, respectively, but the handshake logic of TMRx trigger output is connected to ADC1_CLK. Therefore, when only ADC2_CLK is turned on and ADC1_CLK is turned off, the TMRx trigger output cannot be output normally.

Solutions

When the TRGO event of TMRx is used to trigger the ADC2 regular group acquisition channel, the ADC1 clock needs to be turned on simultaneously.

6 Revision History

Table 2 Document Revision History

Date	Version	Revision History
2023.11	1.0	New edition
2025.6	1.1	Add ADC errata

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